

to -- transmitter phase locked-loop (Tx PLL) and a receiver phase locked-loop (Rx PLL)

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Regarding claim 16, in line 2, "an Rx SIPO" has been changed to -- a receiver serial-in-parallel-out circuitry (Rx SIPO) --.

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8-17-07
Regarding claim ²⁰16, in line 2, "an Rx SIPO" has been changed to -- a receiver serial-in-parallel-out circuitry (Rx SIPO) --.

Response to Arguments

4. Applicant's arguments, see Applicant's Remarks, filed on 07/13/2007, with respect to claims 1-5 have been fully considered and are persuasive. The rejection of claims 1-5 has been withdrawn.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

5. Regarding claim 1, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "at least one regulated power source and at least one unregulated power source, both coupled to selectively provide regulated and unregulated power to the first and second MGT circuitry" and "programmable logic for providing control signals to select and operatively couple the first and second MGT circuitry to one of the at least one regulated and